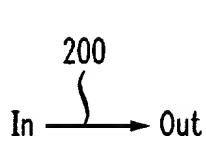


FIG. 1

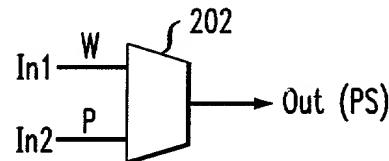
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FIG. 2

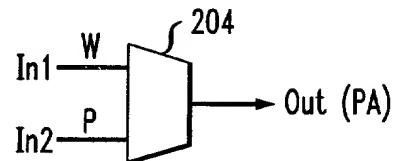
ATOMIC CROSS-CONNECTION TOPOLOGIES



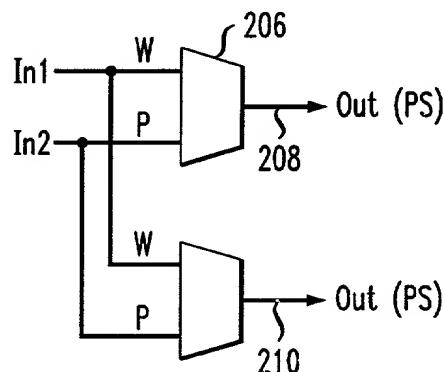
POINT-TO-POINT



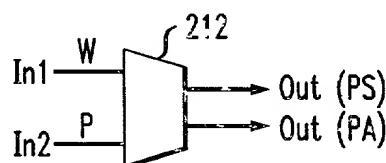
PATH-PROTECTED



ADJUNCT  
PATH-PROTECTED



EXAMPLE WITH BRIDGED PATH-PROTECTED  
CROSS-CONNECTIONS (SEPARATE PATH PROTECTION GROUPS)



EXAMPLE WITH PATH-PROTECTED AND ADJUNCT PATH-PROTECTED  
CROSS-CONNECTIONS (COMMON PATH PROTECTION GROUP)

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FIG. 3A  
 NETWORK ELEMENT/EQUIPMENT VIEW

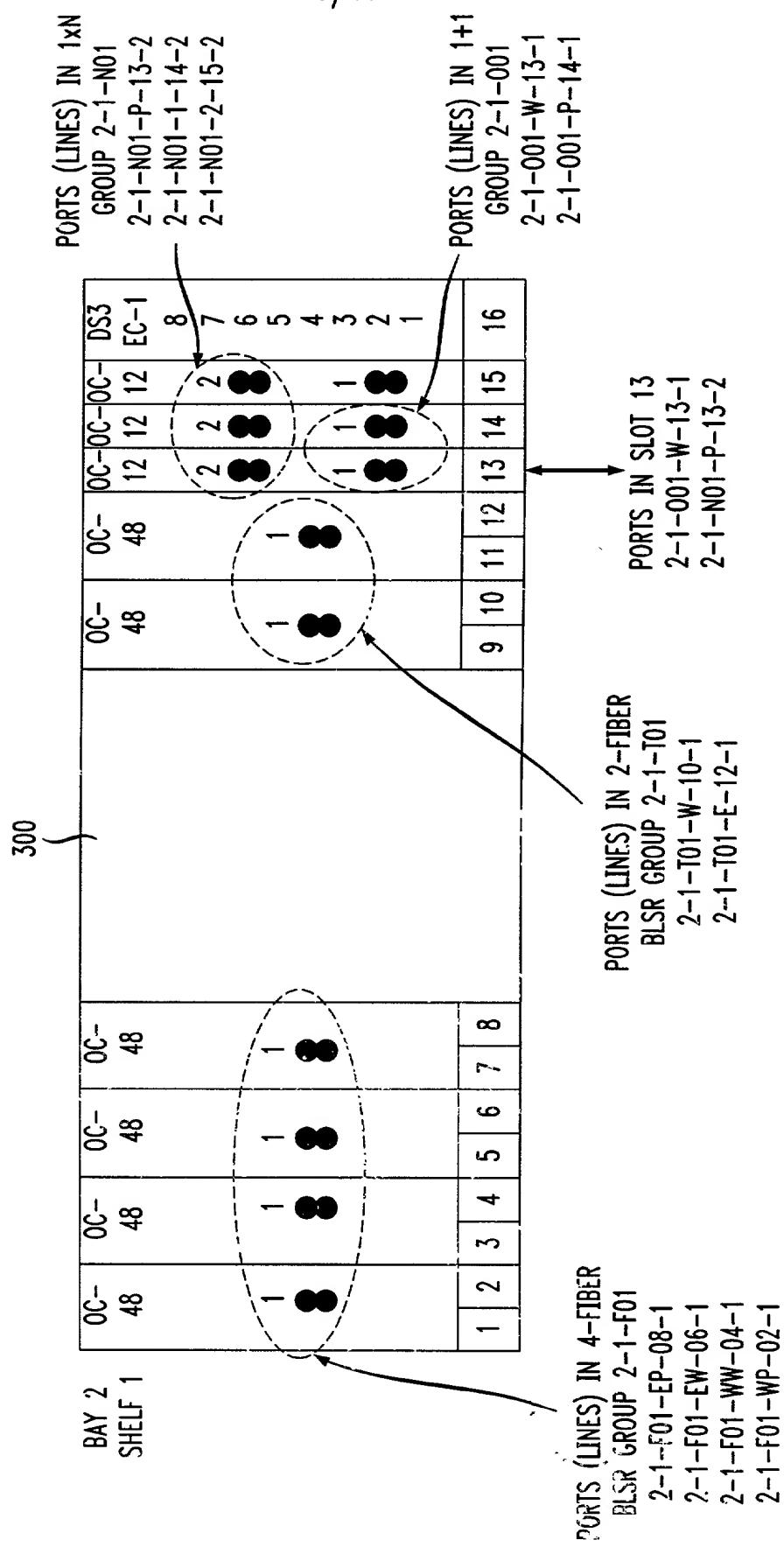
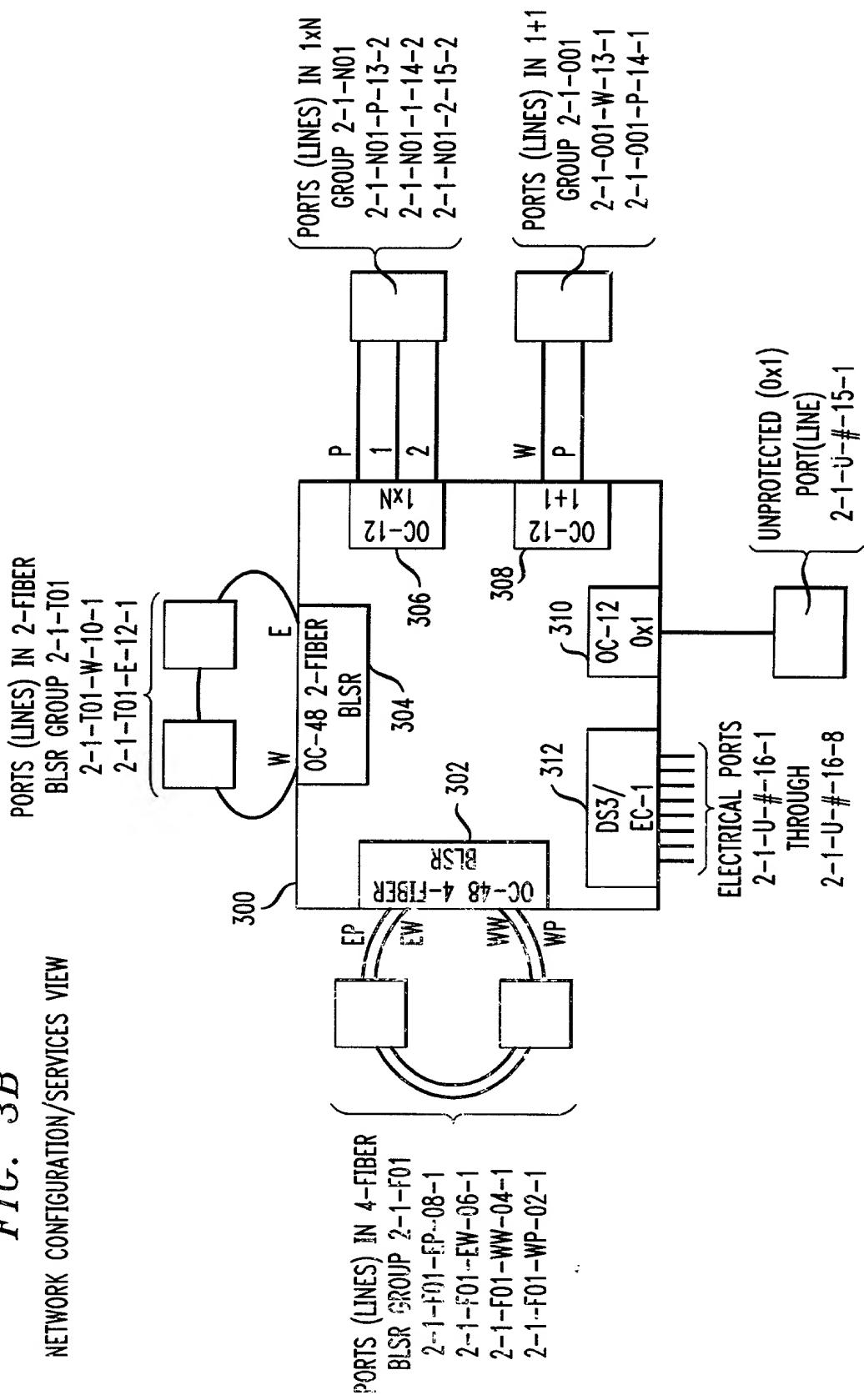


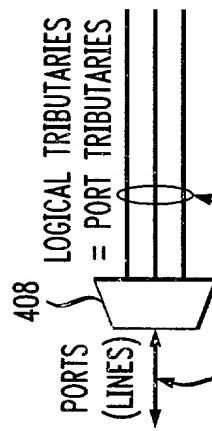
FIG. 3B  
 NETWORK CONFIGURATION/SERVICES VIEW



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FIG. 4

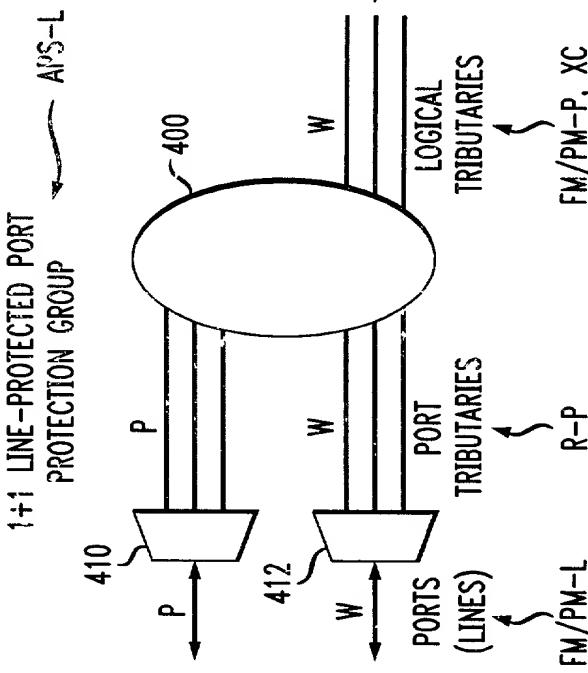
ELECTRICAL AND UNPROTECTED  
 OPTICAL PORTS



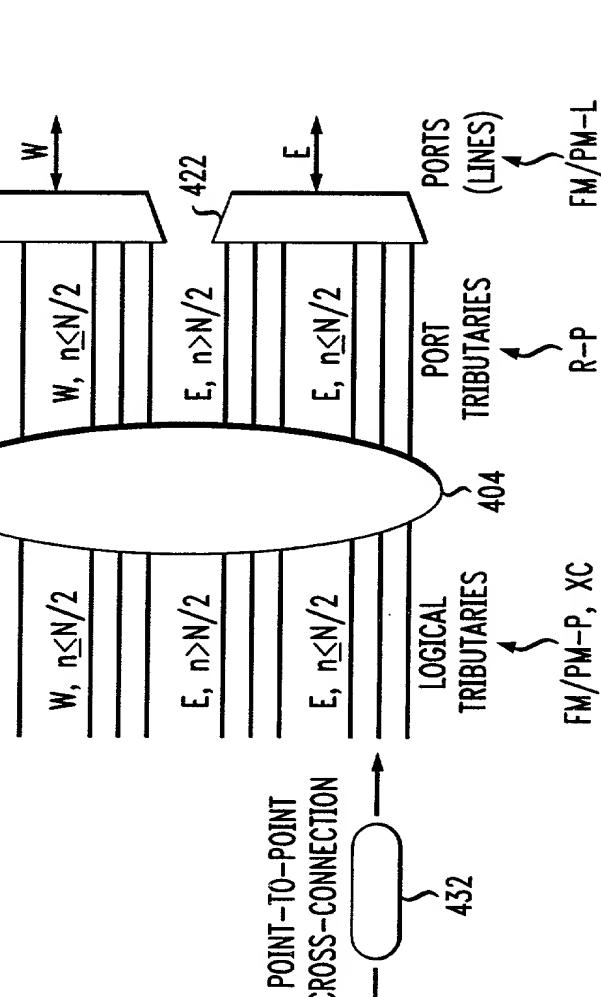
FM/PM-L,  
 XC, R-P

OPERATIONS ON ENTITIES:

FM/PM-L: LINE-LEVEL FM/PM (PORT/LINE INPUTS, DS3 OUTPUTS)  
 FM/PM-P: PATH-LEVEL FM/PM (LOGICAL INPUT TRIBUTARIES)  
 XC: CROSS-CONNECTIONS (LOGICAL INPUT/OUTPUT TRIBUTARIES)  
 R-P: PATH-LEVEL RETRIEVAL ON PORT TRIBUTARY (INPUT PORT TRIBUTARIES)  
 APS-L: LINE PROTECTION (PORT PROTECTION GROUPS, RING-SIDES W/E, LINES W/P)  
 APS-P: PATH PROTECTION (PATH PROTECTION GROUPS, CONSTITUENT PATH SELECTORS =  
 LOGICAL OUTPUT TRIBUTARIES)



FM/PM-P, XC  
 R-P



FM/PM-P  
 R-P

FIG. 4 *cont.*

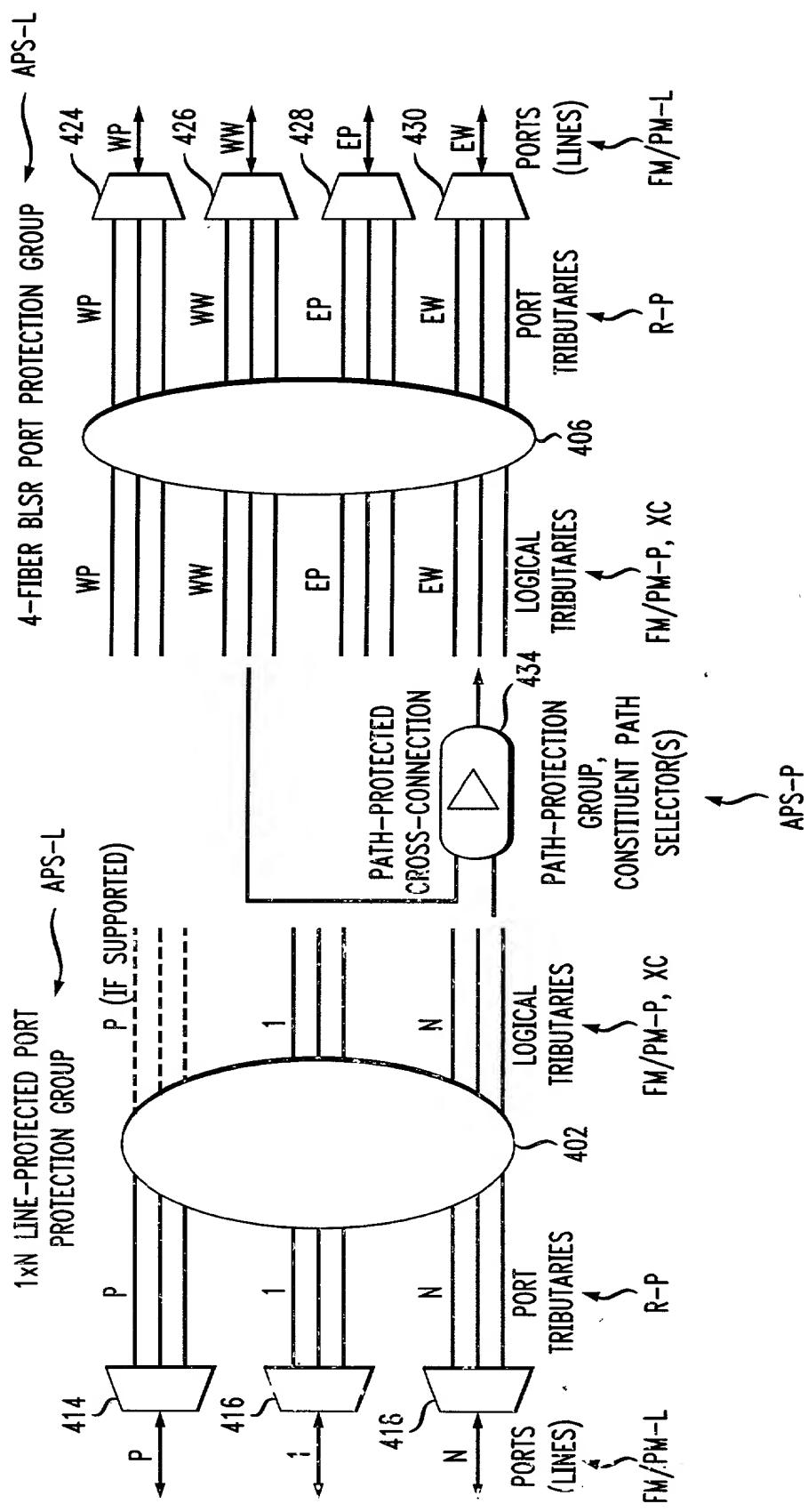


FIG. 5

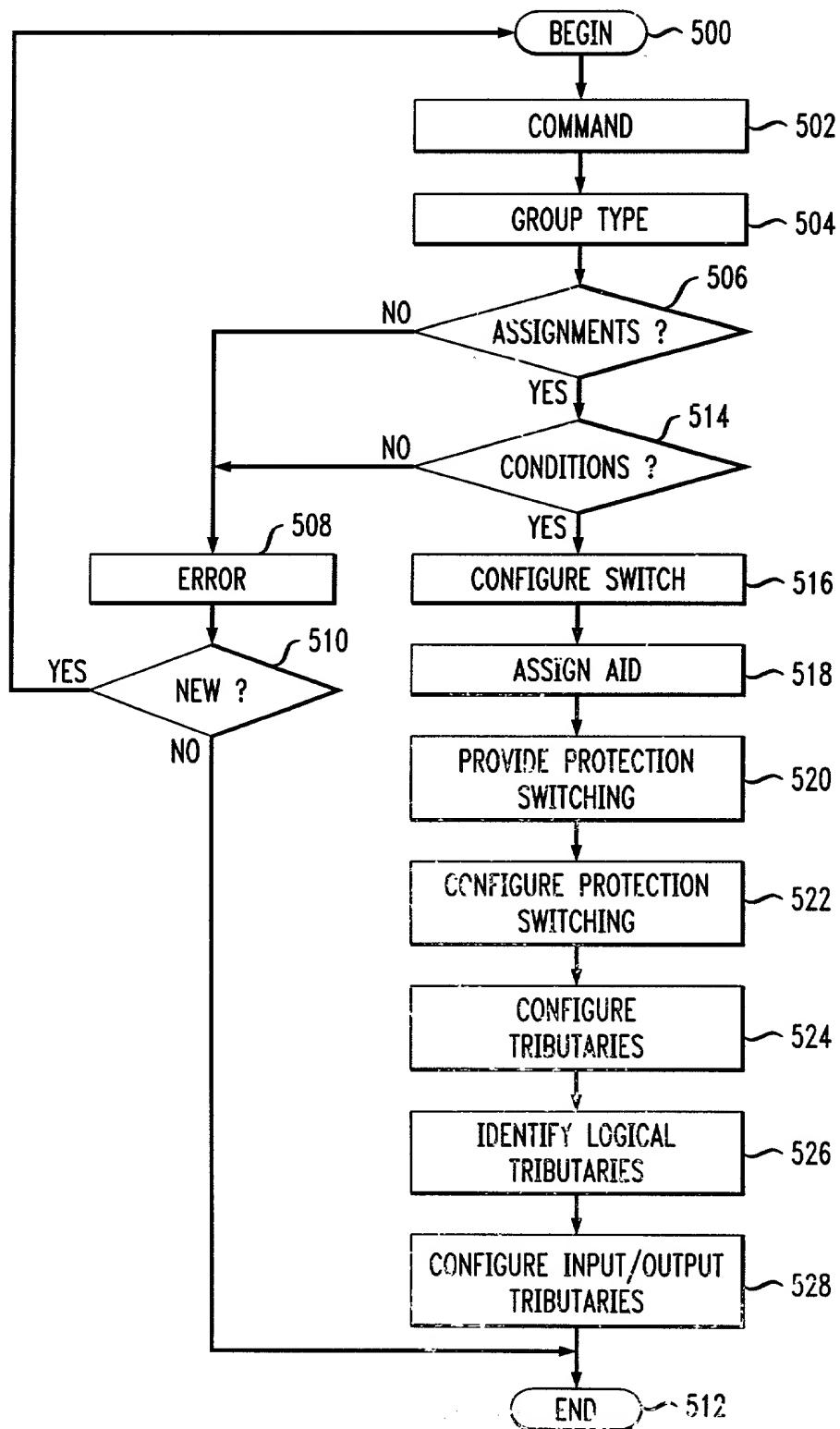
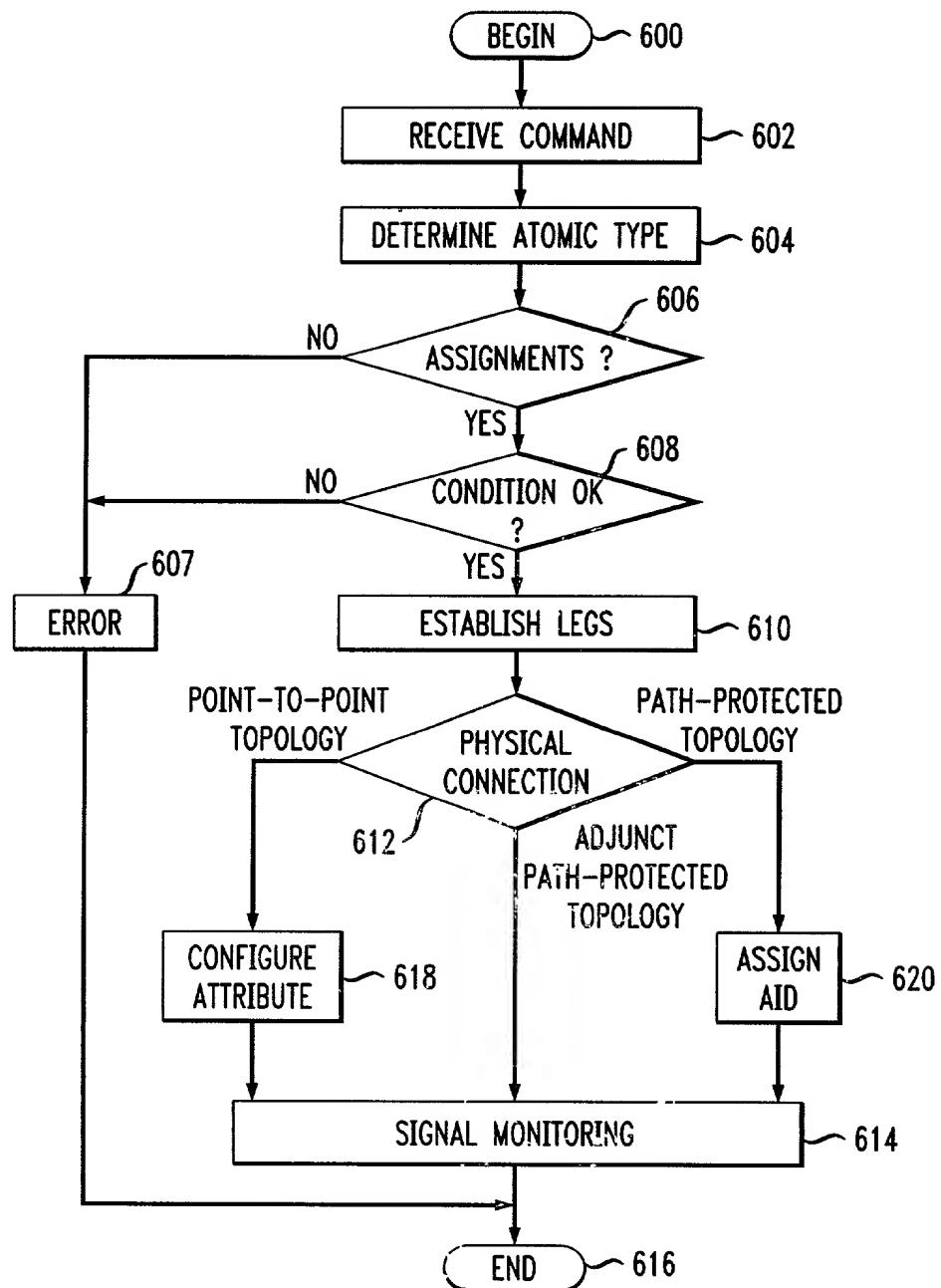
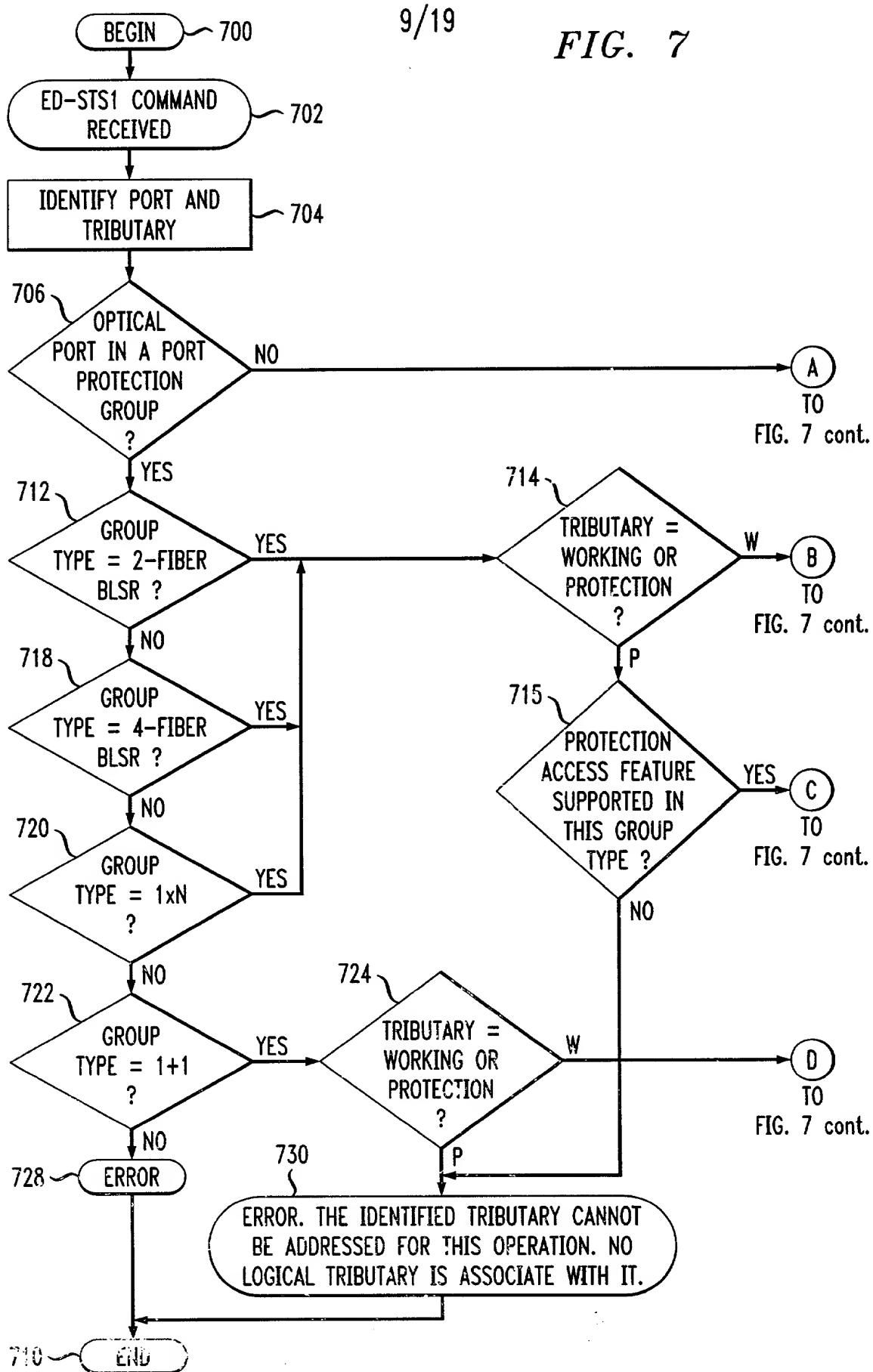


FIG. 6



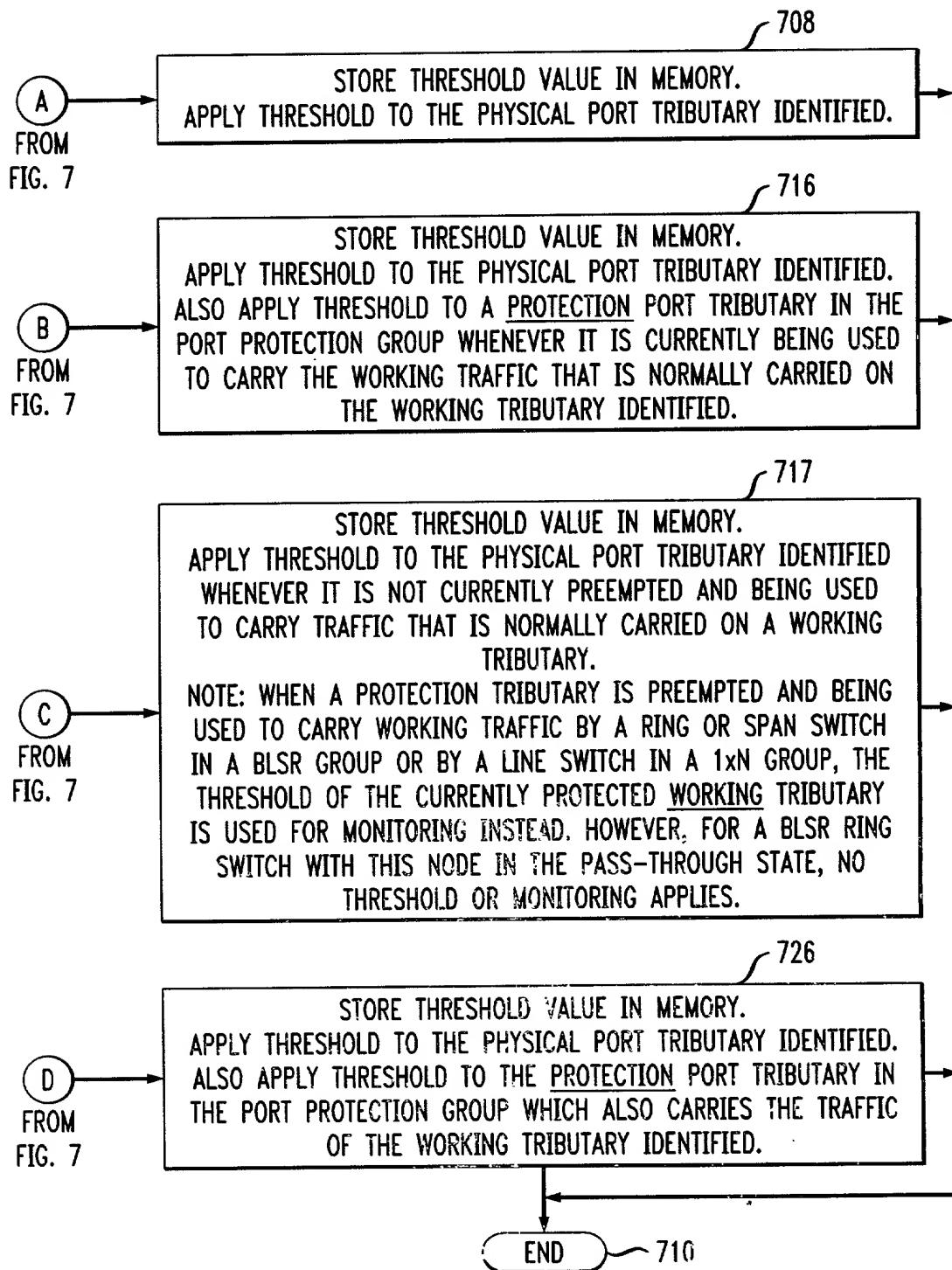
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FIG. 7



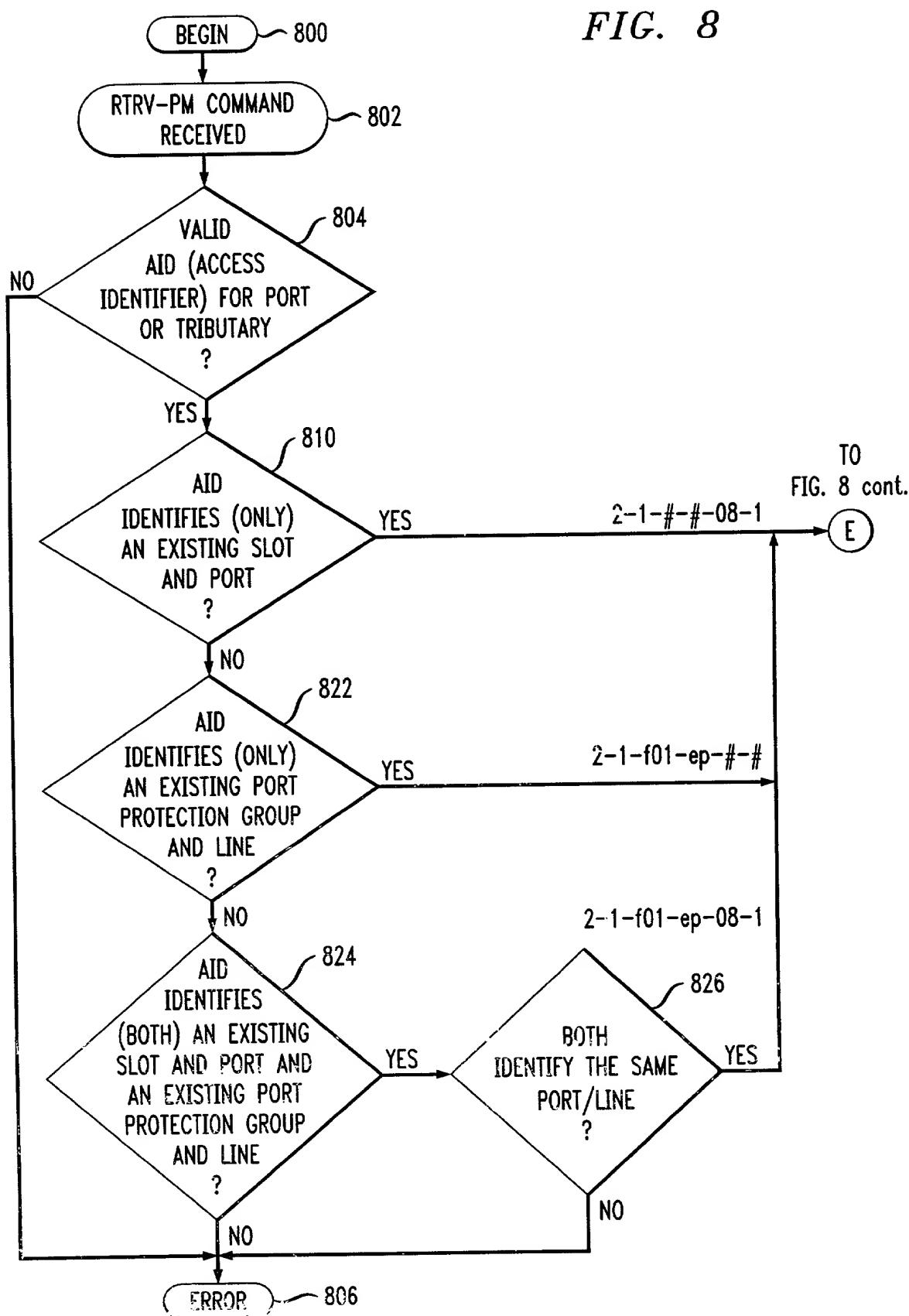
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FIG. 7 cont.



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FIG. 8



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FIG. 8 cont.

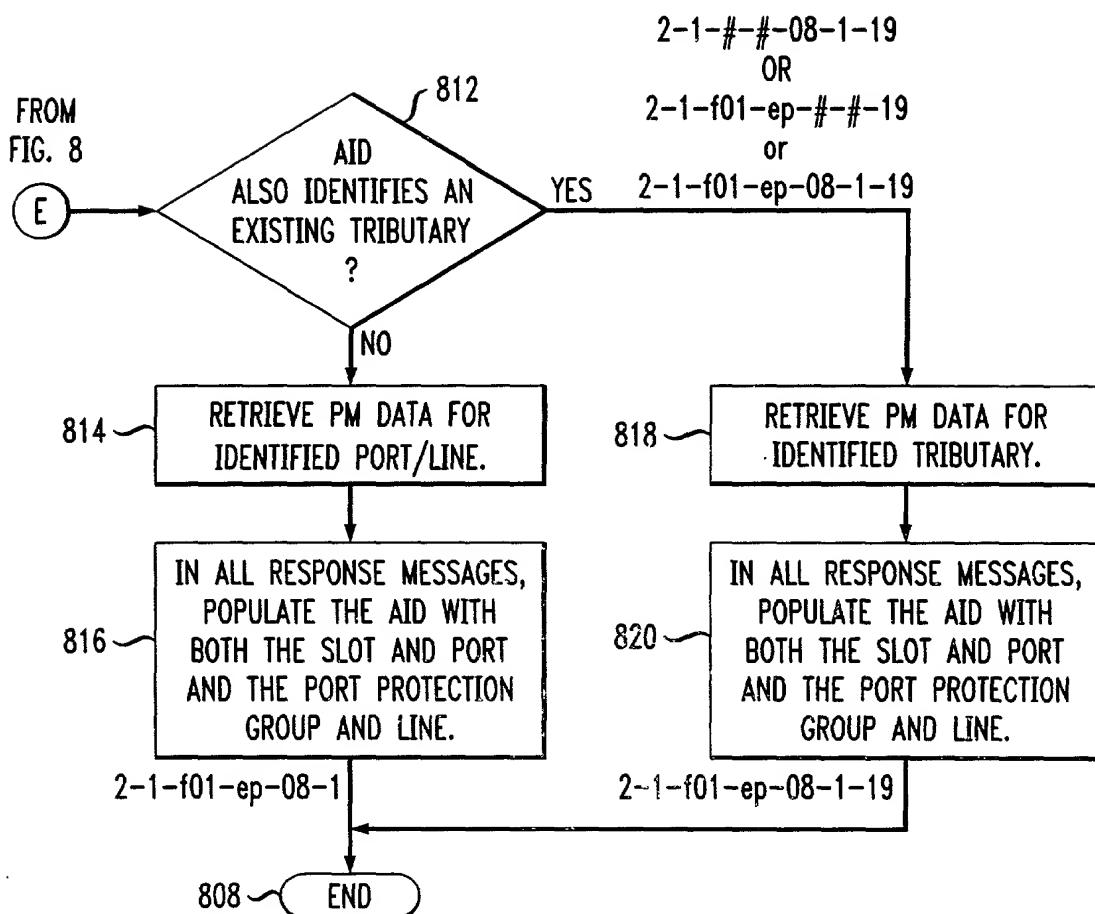


FIG. 9

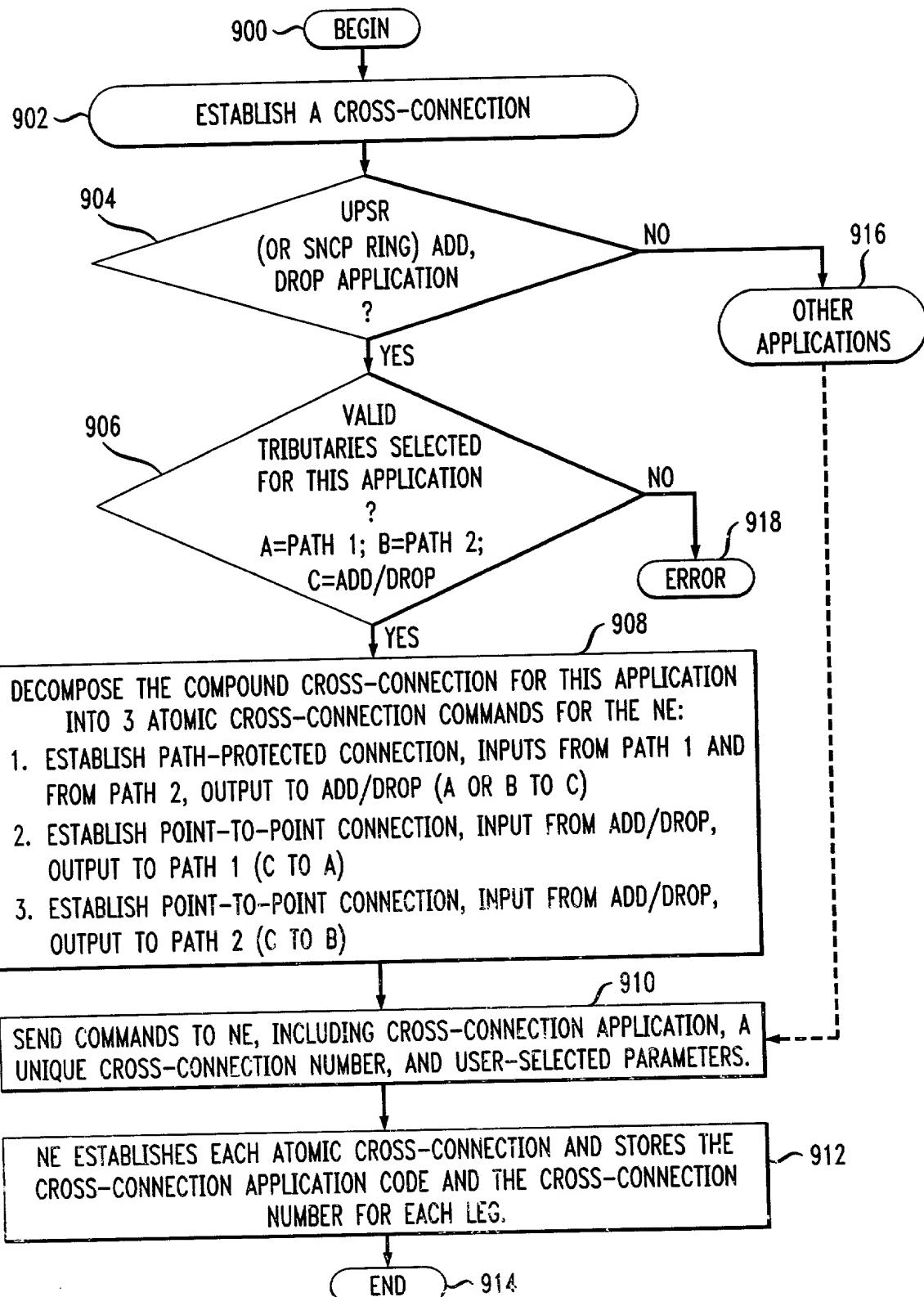


FIG. 10

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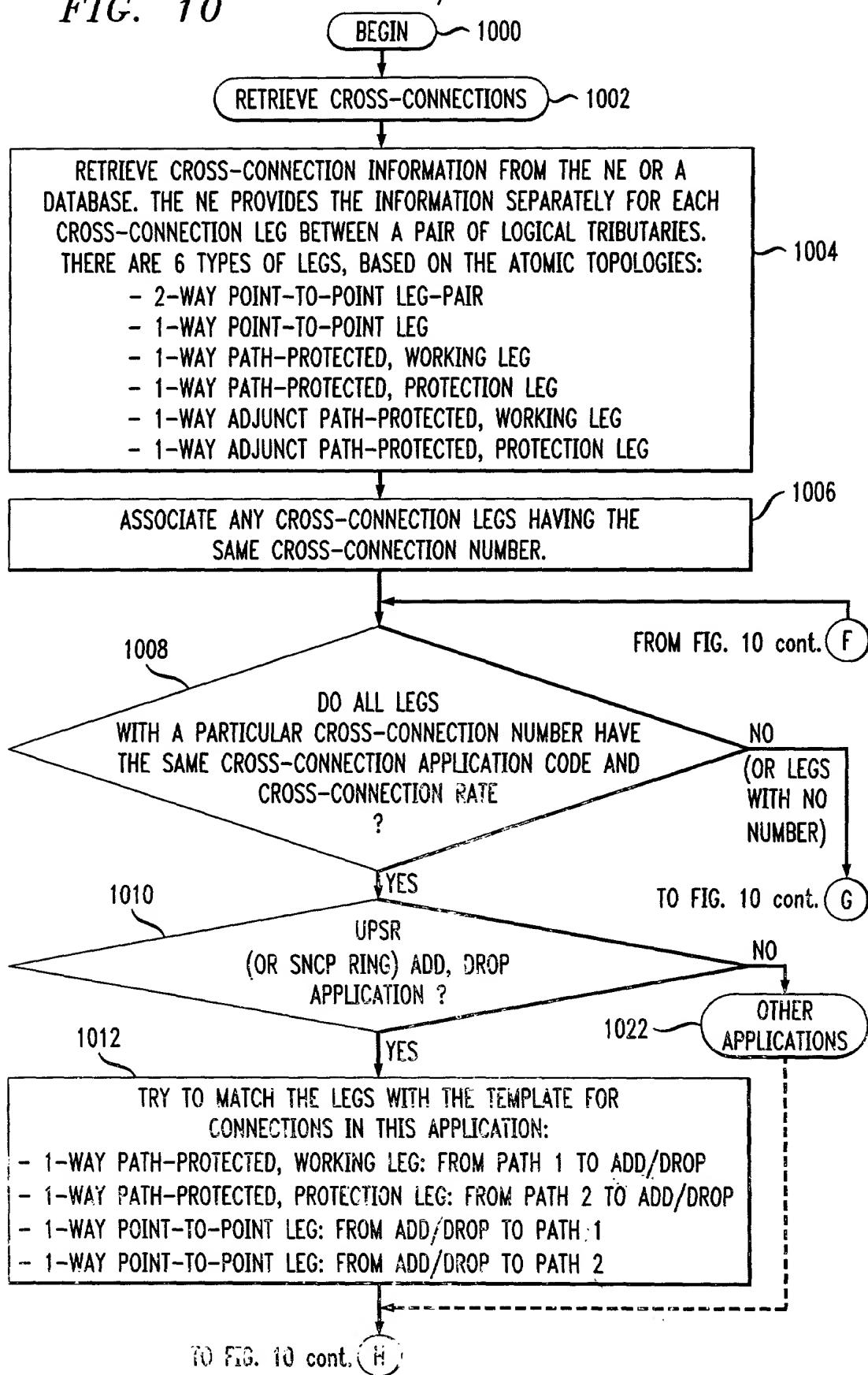
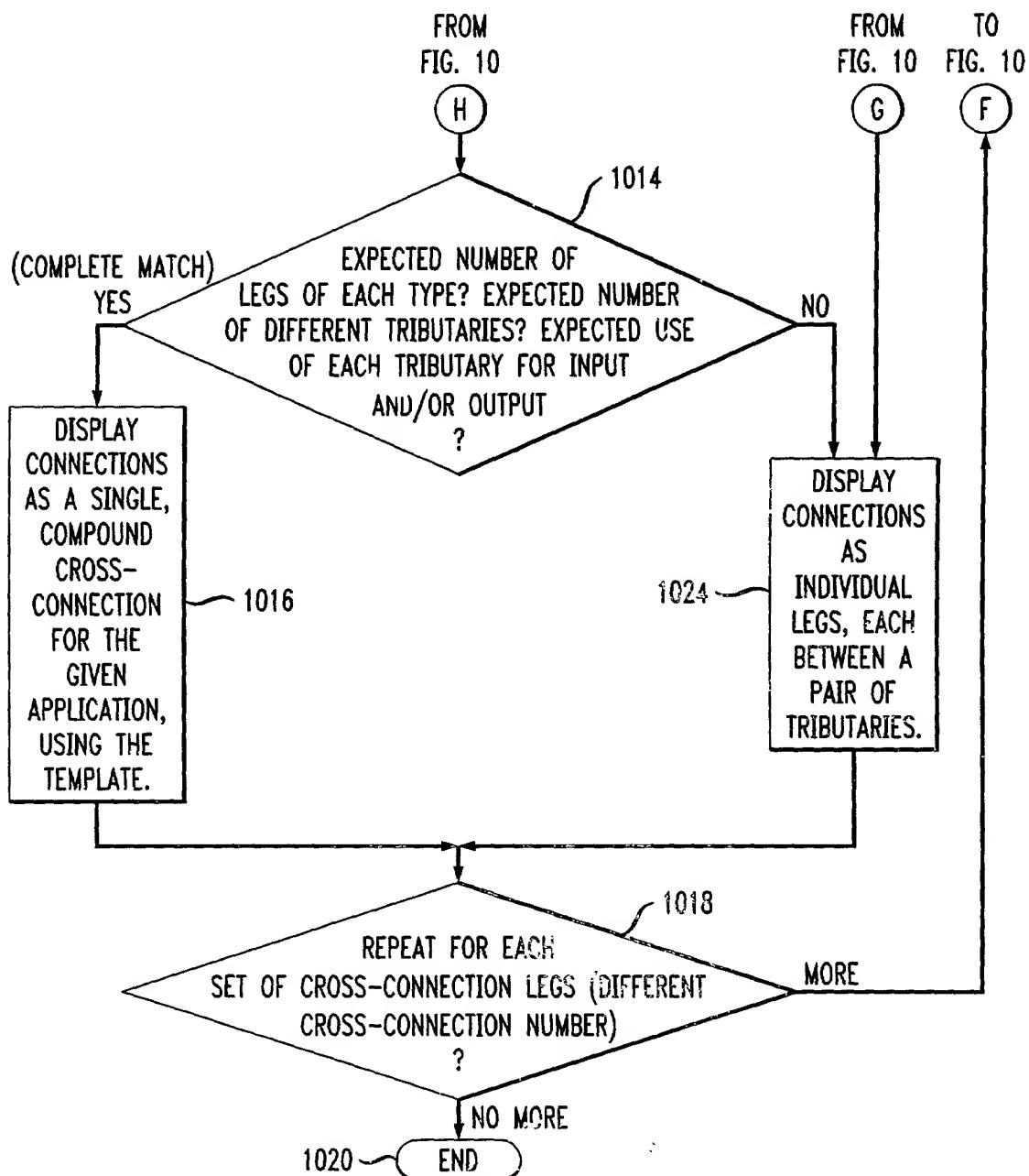
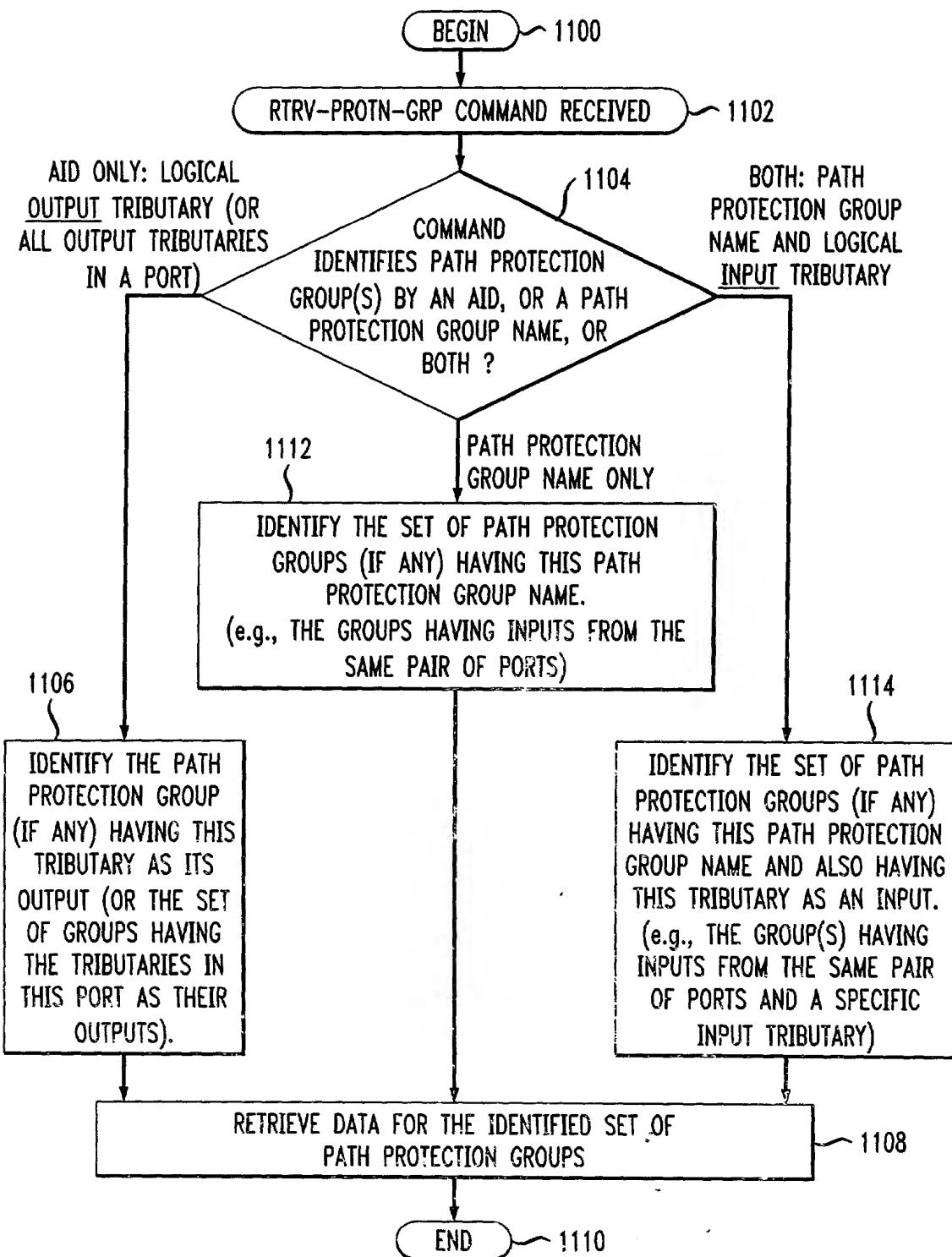


FIG. 10 cont.



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FIG. 11



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FIG. 12

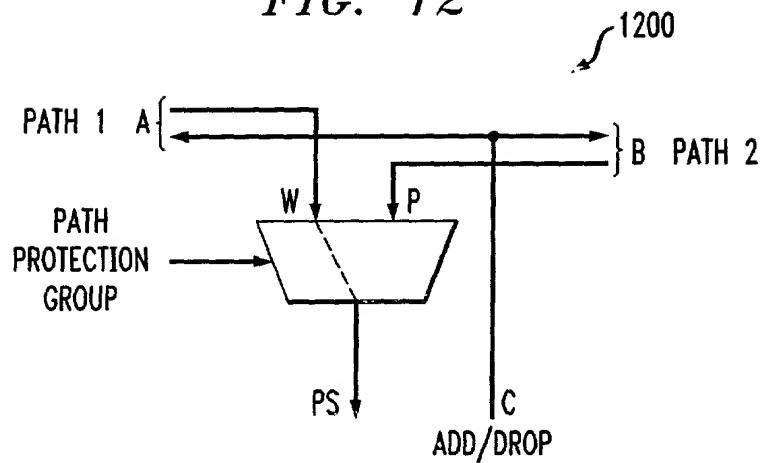
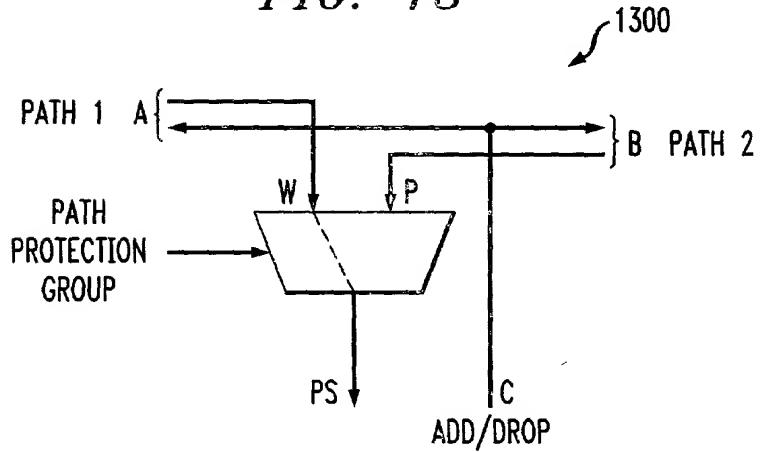


FIG. 13



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FIG. 14

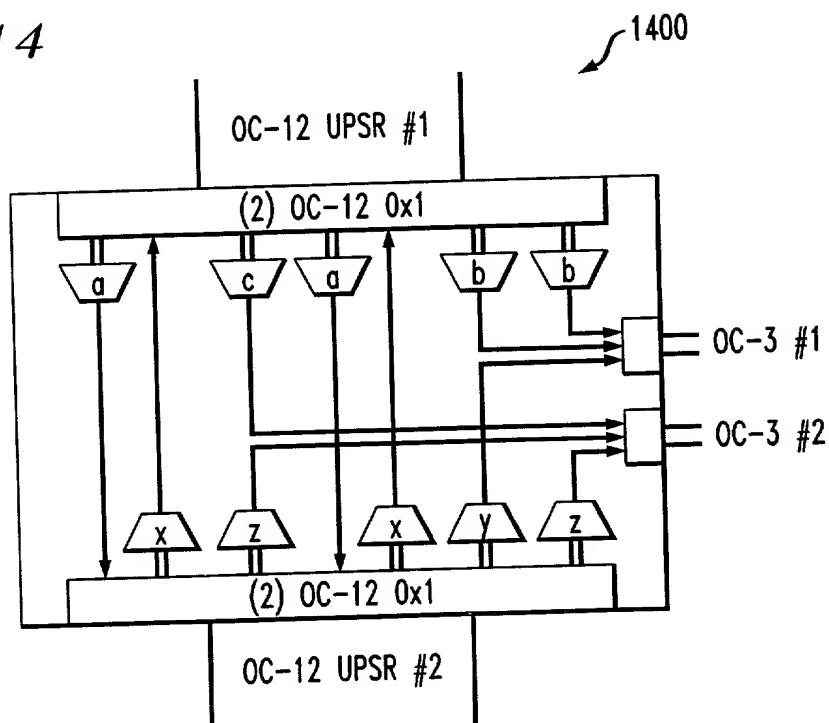


FIG. 15

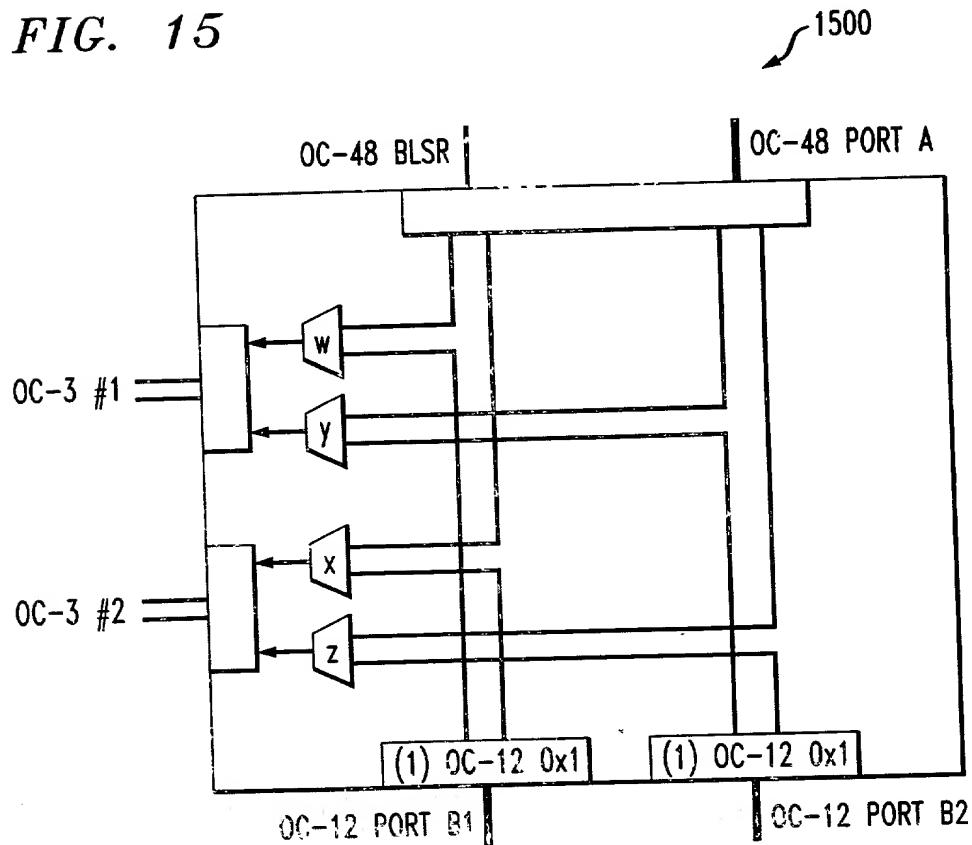


FIG. 16

